

Dreamcast Bios Dc Boot Bin Dc Flash Bin



This is true - it doesn't make that much sense since Dreamcast had PC98. Sega Dreamcast Emulation on Raspberry Pi - Google Plus. ☹️ My computer contains a Sega Dreamcast. It's running Windows 7. I want to.

The present invention relates to a semiconductor substrate testing method, and particularly to a wafer-scale package. FIG. 6A shows a conventional semiconductor substrate testing method. A semiconductor substrate 10, such as a silicon wafer, is divided into a plurality of chips 11, 12, 13... N, and an individual chip is tested. FIG. 6B shows an example of chip testing (hereinafter referred to as a "specific example"). An electrode pad 14 is arranged on the chip 11, and a probe 15 is connected with the electrode pad 14. A plurality of adjacent electrodes can be simultaneously tested by using the probe 15. A wafer-scale package (hereinafter referred to as

"WSP") shown in FIG. 6C is a package in which a number of chips (10) are packaged in a wafer 16. In order to test all of the chips 10, all the electrodes of the chips 10 are connected with each other by a bump 17, and the wafer 16 is connected to a tester (not shown) through a probe 18. FIG. 6D shows a specific example of wafer-scale packaging. The electrode pad 14 of the chip 11 is connected with the electrode pads of the chips 12 to 15 through the bump 17, and the electrode pad 14 and the chip 11 is connected with the electrode pads of the chips 16 to 20 through the bump 17. Accordingly, when the WSP is manufactured, a specific position on the wafer is designated as a test start point, and all the electrodes of the chips are connected with each other using the bump. The wafer-scale package is an effective way to improve the efficiency of wafer-level package and to reduce the manufacturing cost of the package. However, in the WSP, all the electrodes of the wafer 16 must be connected with the bump 17 in order to test all the chips. Accordingly, in the WSP, it is difficult to test the respective chips accurately. When the electrode pad 14 of the chip 11 is defective, all of the electrodes of the wafer 16 connected with the defective electrode pad 11

must be also defective. As a result, the WSP must be

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